

What is claim d is:

1. An apparatus comprising
a configuration store to select between an encoded chip select mode and
an unencoded chip select mode, and
an address decoder to generate unencoded chip select words in response
to the unencoded chip select mode and to generate encoded chip select words in
response to the the encoded chip select mode.
2. The apparatus of claim 1 wherein the address decoder in response to
an address for a boot code nub generates a chip select word that selects the
same memory device regardless of operating in the unencoded chip select mode
or the encoded chip select mode.
3. The apparatus of claim 1 wherein the address decoder in response to
an address for a boot code nub
generates an encoded chip select word that selects a predetermined
memory device that comprises the boot code nub when in the encoded chip
select mode, and
generates an unencoded chip select that selects the predetermined
memory device that comprises the boot code nub when in the unencoded chip
select mode.
4. The apparatus of claim 1 wherein the address decoder, in response to
an address for a boot code nub, generates the unencoded chip select word for

the address such that the unencoded chip select word comprises the encoded chip select word for the address.

5. The apparatus of claim 1 wherein the address decoder, in response to an address for a boot code nub,

generates the encoded chip select word for the address such that the encoded chip select word comprises exactly one active chip select bit, and

generates the unencoded chip select word for the address such that the unencoded chip select word comprises exactly one active chip select bit.

6. The apparatus of claim 1 wherein the address decoder, in response to an address for a boot code nub,

generates the encoded chip select word for the address such that the encoded chip select word comprises exactly one active chip select bit that corresponds to a predetermined chip select line used to select a memory device comprising the boot code nub, and

generates the unencoded chip select word for the address such that the unencoded chip select word comprises exactly one active chip select bit that corresponds to the predetermined chip select line.

7. The apparatus of claim 1 wherein the address decoder, in response to an address for a boot code nub,

generates the encoded chip select word such that a lowest order bit of the encoded chip select word is the only active bit of the encoded chip select word, and

generates the unencoded chip select word such that a lowest order bit of the unencoded chip select word is the only active bit of the unencoded chip select word.

8. A system comprising
a plurality of memory devices comprising a memory device with a boot code nub, and
an apparatus

to generate, in response to an address of a boot code nub and an encoded chip select mode, an encoded chip select word that selects the memory device with the boot code nub, and

to generate, in response to the address of the boot code nub and an unencoded chip select mode, an unencoded chip select word that comprises the encoded chip select word of the boot code nub.

9. The system of claim 8 wherein
the memory device with the boot code nub is coupled to the apparatus via a predetermined chip select line, and

each of the other memory devices of the plurality of memory devices is coupled to the apparatus via a separate chip select line.

10. The system of claim 8 further comprising a chip select decoder coupled to the apparatus and coupled to each of the memory devices of the plurality of memory devices via a separate chip select line, wherein

the chip select decoder activates the chip select line of the memory device with the boot code nub in response to receiving the encoded chip select word for the address from the apparatus.

11. The system of claim 10 wherein the chip select decoder activates the chip select line of the memory device with the boot code nub in response to receiving the unencoded chip select word for the address from the apparatus.

12. The system of claim 8 further comprising a chip select decoder coupled to the apparatus and coupled to each of the memory devices of the plurality of memory devices via a separate chip select line, wherein

the chip select decoder activates the chip select line of the memory device with the boot code nub in response to receiving the encoded chip select word for the address of the boot code nub from the unencoded chip select word generated by the apparatus for the address of the boot code nub.

13. A method comprising
generating, in response to an address of a boot code nub and an encoded chip select mode, an encoded chip select word that selects a memory device with the boot code nub, and

generating, in response to the address of the boot code nub and an unencoded chip select mode, an unencoded chip select word that comprises the encoded chip select word of the boot code nub.

14. The method of claim 13 further comprising
updating an operation mode to one of the encoded chip select mode and the unencoded chip select mode.

15. The method of claim 13 further comprising
executing the boot code nub, and
updating an operation mode to one of the encoded chip select mode and
the unencoded chip select mode in response to executing the boot code nub.

16. The method of claim 13 further comprising
executing the boot code nub,
updating an operation mode to the encoded chip select mode in response
to executing the boot code nub, and
reassigning chip select pins not used to carry encoded chip select words
after updating the operation mode.

17. The method of claim 13 wherein
generating the encoded chip select word comprises generating the
encoded chip select word such that the encoded chip select word comprises
exactly one active chip select bit, and
generating the unencoded chip select word comprises generating the
unencoded chip select word such that the unencoded chip select word comprises
exactly one active chip select bit.

18. The method of claim 13 wherein
generating the encoded chip select word comprises generating the
encoded chip select word such that the encoded chip select word comprises
exactly one active chip select bit that corresponds to a predetermined chip select
line used to select the memory device with the boot code nub, and

generating the unencoded chip select word comprises generating the unencoded chip select word such that the unencoded chip select word comprises exactly one active chip select bit that corresponds to the predetermined chip select line.

19. The method of claim 13 wherein

generating the encoded chip select word comprises generating the encoded chip select word such that a lowest order bit of the encoded chip select word is the only active bit of the encoded chip select word, and

generating the unencoded chip select word comprises generating the unencoded chip select word such that a lowest order bit of the unencoded chip select word is the only active bit of the unencoded chip select word.

20. A machine readable medium comprising a plurality of instructions that, in response to being executed result, in an apparatus

generating, in response to an address of a boot code nub and an encoded chip select mode, an encoded chip select word that selects a memory device with the boot code nub, and

generating, in response to the address of the boot code nub and an unencoded chip select mode, an unencoded chip select word that comprises the encoded chip select word of the boot code nub.

21. The machine readable medium of claim 20 wherein the plurality of instructions further result in the apparatus

generating the encoded chip select word such that the encoded chip select word comprises exactly one active chip select bit, and

generating the unencoded chip select word such that the unencoded chip select word comprises exactly one active chip select bit.

22. The machine readable medium of claim 20 wherein the plurality of instructions further result in the apparatus

generating the encoded chip select word such that the encoded chip select word comprises exactly one active chip select bit that corresponds to a predetermined chip select line used to select the memory device with the boot code nub, and

generating the unencoded chip select word such that the unencoded chip select word comprises exactly one active chip select bit that corresponds to the predetermined chip select line.

23. The machine readable medium of claim 20 wherein the plurality of instructions further result in the apparatus

generating the encoded chip select word such that a lowest order bit of the encoded chip select word is the only active bit of the encoded chip select word, and

generating the unencoded chip select word such that a lowest order bit of the unencoded chip select word is the only active bit of the unencoded chip select word.